

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Andrew J. Burstein, et al. Art Unit : Unknown
 Serial No. : Uassigned Examiner : Unknown
 Filed : June 26, 2001
 Title : TRANSISTOR PATTERN FOR VOLTAGE REGULATOR

Commissioner for Patents
 Washington, D.C. 20231

D. #2
 Mon 1/1/01
 Re-Amendtr

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

In the title:

Please replace the title "A FLIP-CHIP SWITCHING REGULATOR" with
 --TRANSISTOR PATTERN FOR VOLTAGE REGULATOR--

In the specification:

Page 1, line 2, insert the following paragraph:

This application is a continuation of U.S. Patent Application Serial No. 09/498,297, filed
 February 4, 2000, the entirety of which is incorporated by reference.

In the claims:

Please cancel claims 1-45.

Please add the following new claims:

46. (New) An integrated circuit structure comprising:

a substrate having a first plurality of doped regions and a second plurality of doped regions, the first plurality of doped regions and the second plurality of doped regions being arranged in an alternating pattern; and

a gate region on the substrate separating the first plurality of doped regions and the second plurality of doped regions.

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